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... Albeno Sangiovanni-Vincentelli University of California at Berkeley Berkeley, CA, USA alberto ... filling the performance estimation gap in the POLIS [II embedded ...

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... , S Dey, L Lavagno, A Sangiovanni-Vincentelli - Proc. International Workshop on Hardware/Software Codesign (..., 1998 - doi.ieeecs.org

... Lavagno, C. Passerone, A. Sangiovanni-Vincentelli, E. Sentovich, K. Suzuki, and B. Tabbara, Hardware-software Co-Design of Embedded Systems: The POLIS Approach. ...

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... Alberto Sangiovanni-Vincentelli, Dept ... existing codesign environment for reactive embedded systems, called POLIS, described in [CGHS94] and available at URL [hp ...

Cited by 23 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

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... , L Lavagno, C Sansoè, A Sangiovanni-Vincentelli - Design Automation for Embedded Systems, 1998 - Springer

... Passerone, K. Suzuki, and A. Sangiovanni-Vincentelli, Hardware-Software Co-design of Embedded Systems—The POLIS Approach, Kluwer Academic Publishers, 1997. ...

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B Tabbara, L Lavagno, A Sangiovanni-Vincentelli - Proc. Int. High Level Design Validation and Test Wkshp, 1997 - www-cad.eecs.berkeley.edu

... B. Tabbara L. Lavagno A. Sangiovanni-Vincentelli ... We use the POLIS codesign environment for reactive embedded systems ([3][5]) for synthesizing software and ...

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